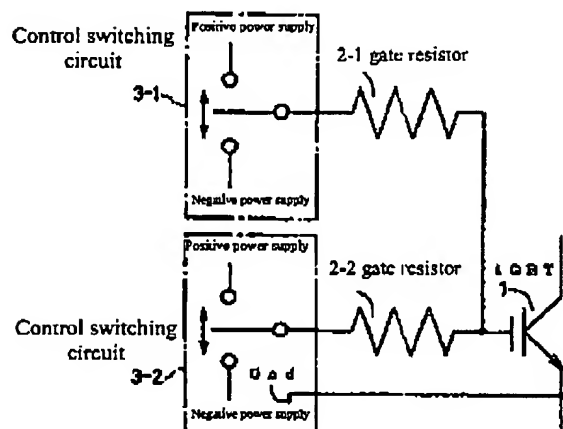


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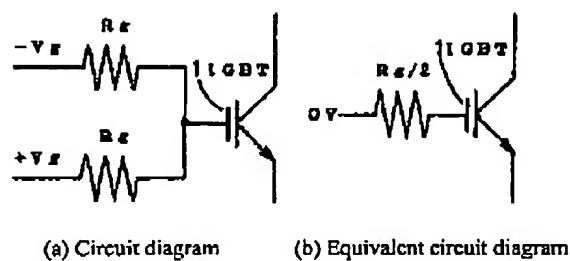
<Figure 1>

Circuit configuration of an embodiment



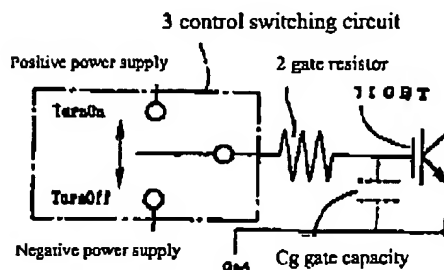
<Figure 2>

Description of turn-off operation of an embodiment



<Figure 6>

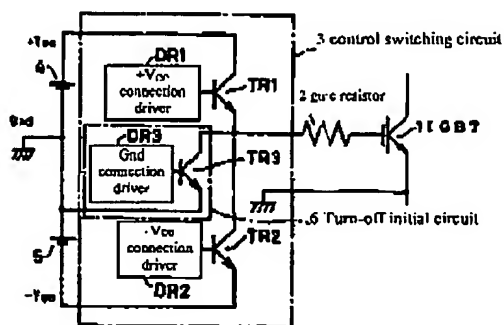
IGBT gate peripheral circuit



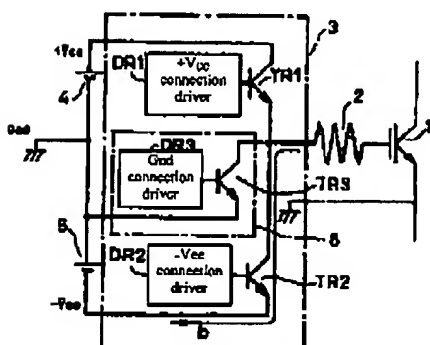
Japanese Unexamined Patent Application Publication H10-75164

<Figure 3>

Circuit configurations for other embodiments

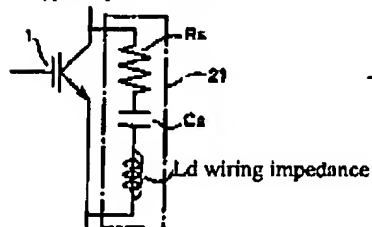


<Figure 5>

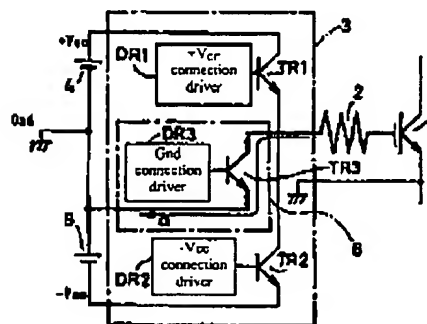
Description of operations of other embodiments
(With the $-V_{cc}$ connection driver turned on.)

<Figure 8>

Typical spanner circuit

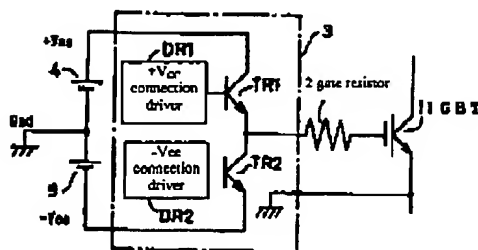


<Figure 4>

Description of operations of other embodiments
(With only the turn-off initial circuit 6 turned on)

<Figure 7>

Conventional gate driver circuit



<Figure 9>

Other typical spanner circuit

